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PATENT NUMBER and ISSUE DATE

## U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER		
10083328	10/22/2001	313	512	2879	Hances		
		II Thomas		Frank	; Woolverton Douglas; Pashley		
Michael;							
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PG-PUB D	O NOT PUBLISH		yes □ no	JIND 4	ATTORNEY DOCKET NO		
Foreign priority	y claimed anditions met		yes □ no		US010528		
Verified and Acknowledged Examiners's initials  TITLE: Led chip package with four led chips and intergrated optics for collimating and mixing the light  U.S.DEPT. OF COMM.PAT. & TM-PTO-435L(Rev. 12-94)							
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	Assistant Examiner	Total Claims Print Claim for O.G			
ISSUE FEE Amount Due Date Paid		DRAWING Sheets Drwg. Figs. Drwg. Print Fig.			
TERMINAL	Primary Examiner  PREPARED FOR ISSUE  WARNING: The information disclose	Application Examiner			
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